

HJ

JCT35 U.S. PTO
09/299659
04/27/99

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION: Yutaka TERADA, et al.

SERIAL NO.: Not Yet Assigned

FILING DATE: 27 April 1999

**RE: INPUT CIRCUIT AND OUTPUT CIRCUIT
INFORMATION DISCLOSURE STATEMENT**

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

In accordance with the duty of disclosure under 37 C.F.R. §§ 1.56 and 1.98, Applicant(s) wish(es) to bring to the attention of the U.S. Patent and Trademark Office the reference(s) listed on the enclosed Form PTO-1449. Copies of the listed reference(s) are attached.

The Commissioner is hereby authorized to charge any additional fees associated with this communication or credit any overpayment to Deposit Account No. 13-0203. A duplicate copy of this communication is enclosed for accounting purposes.

Respectfully submitted,
McDERMOTT, WILL & EMERY

Date: 27 April 1999

By: *MEF*
Michael E. Fogarty
Registration No. 36,139

600 13th Street, N.W.
Washington, D.C. 20005-3096
Telephone: 202-756-8000
Facsimile: 202-756-8087